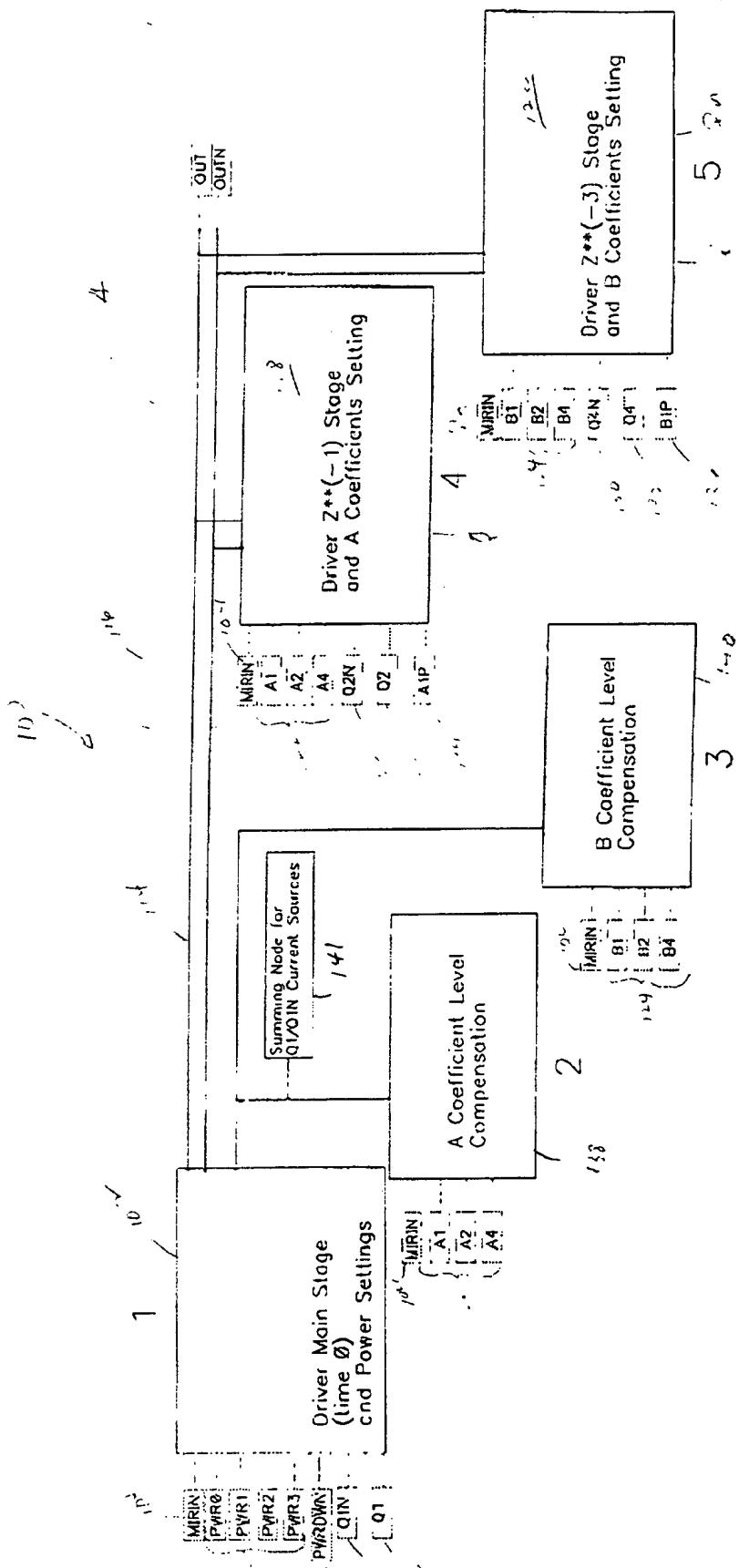


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Figure 1

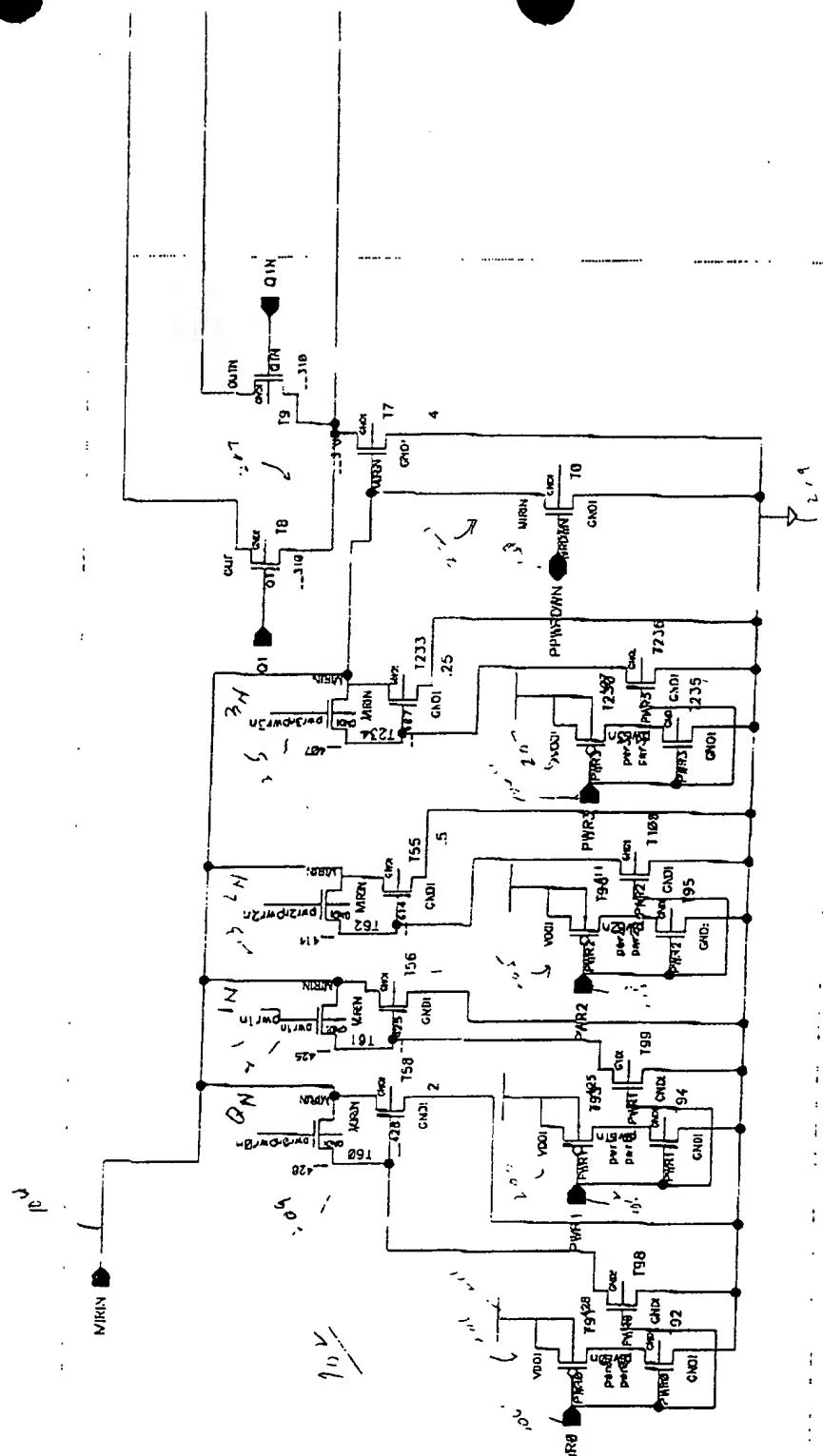


Driver & Power Settings

WIRING

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Figure 2



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Figure 3

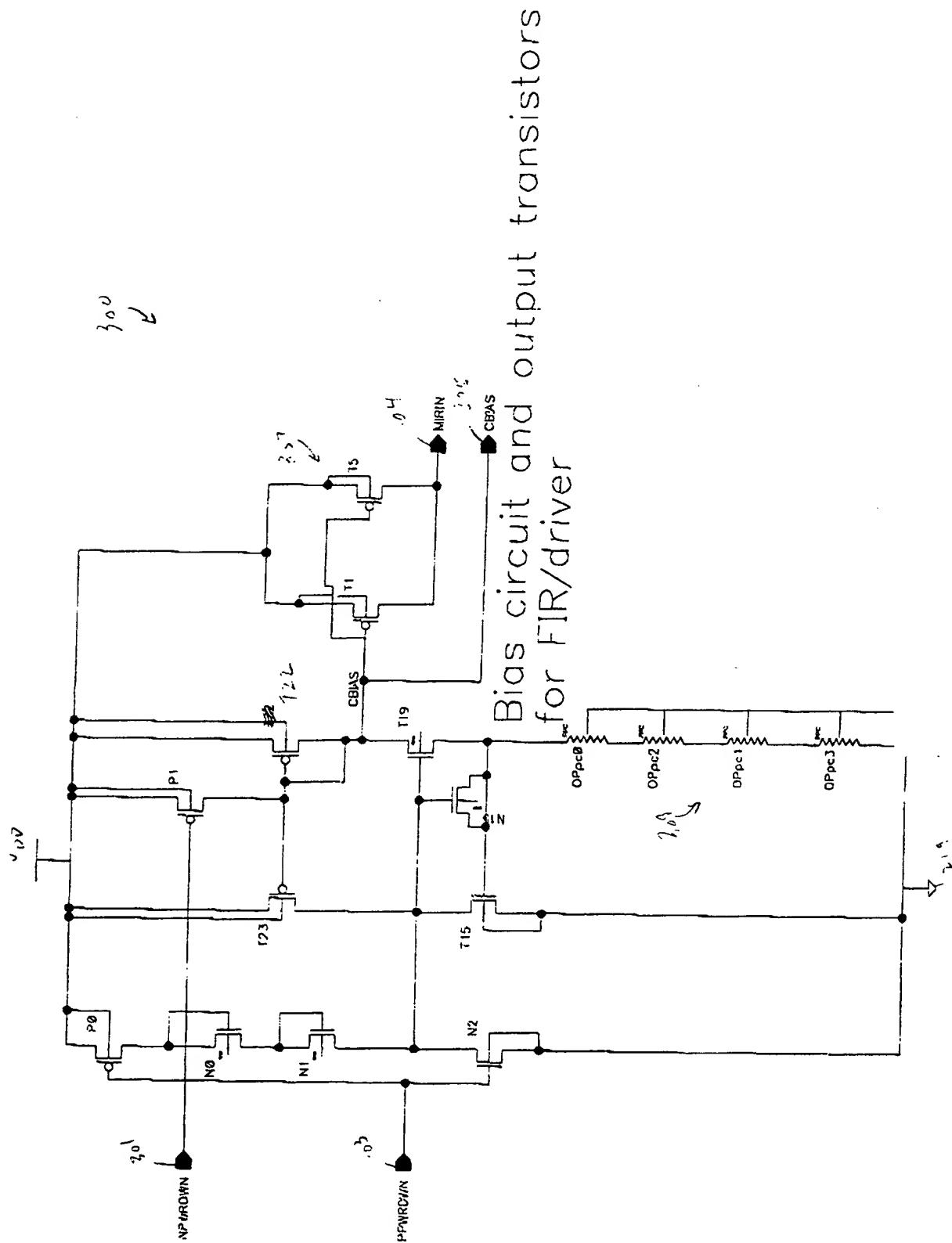
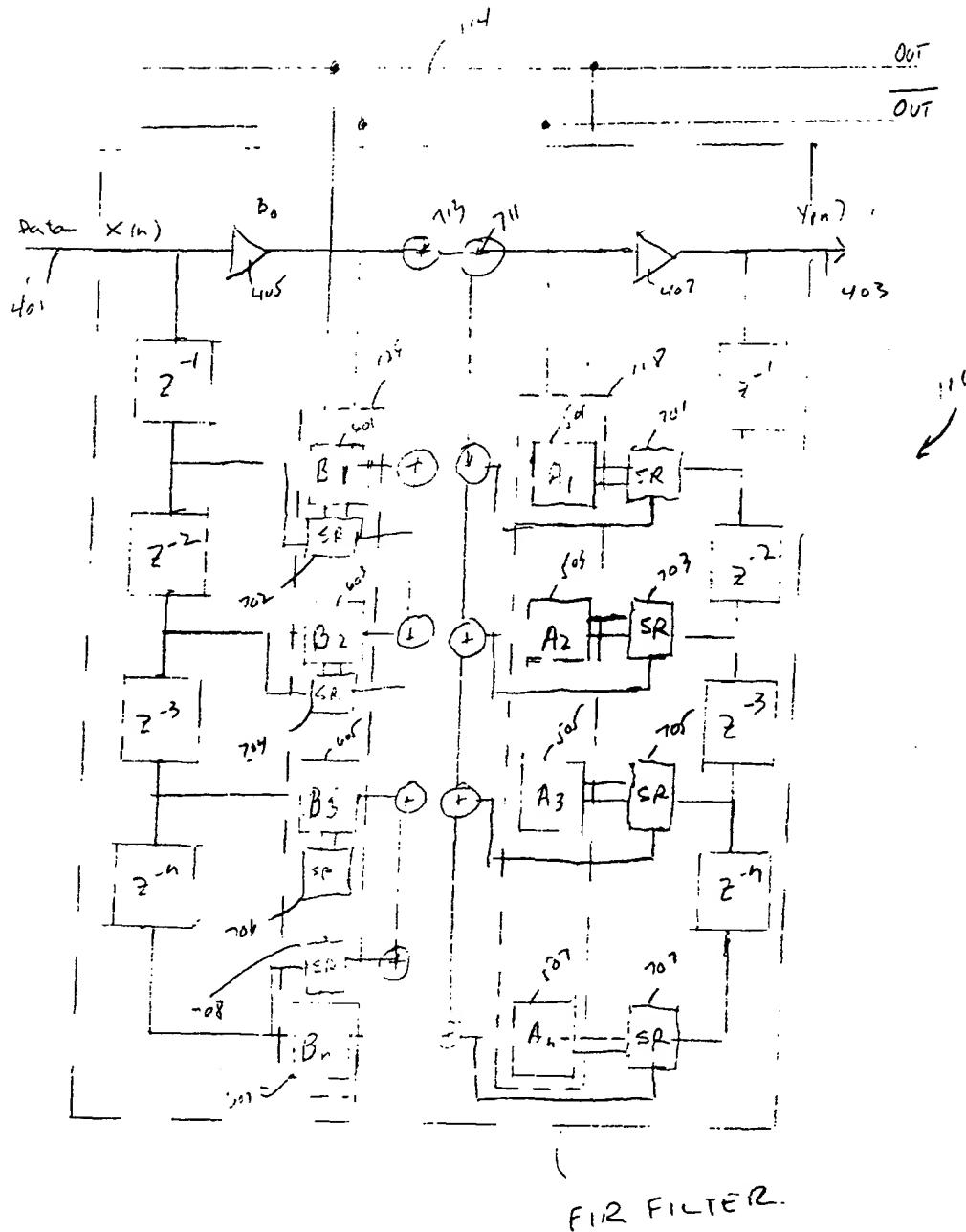


Figure 4



F1/2 FILTER

$$H(z) = A_1 z^{-1} + A_2 z^{-2} + A_3 z^{-3}$$

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Figure 5

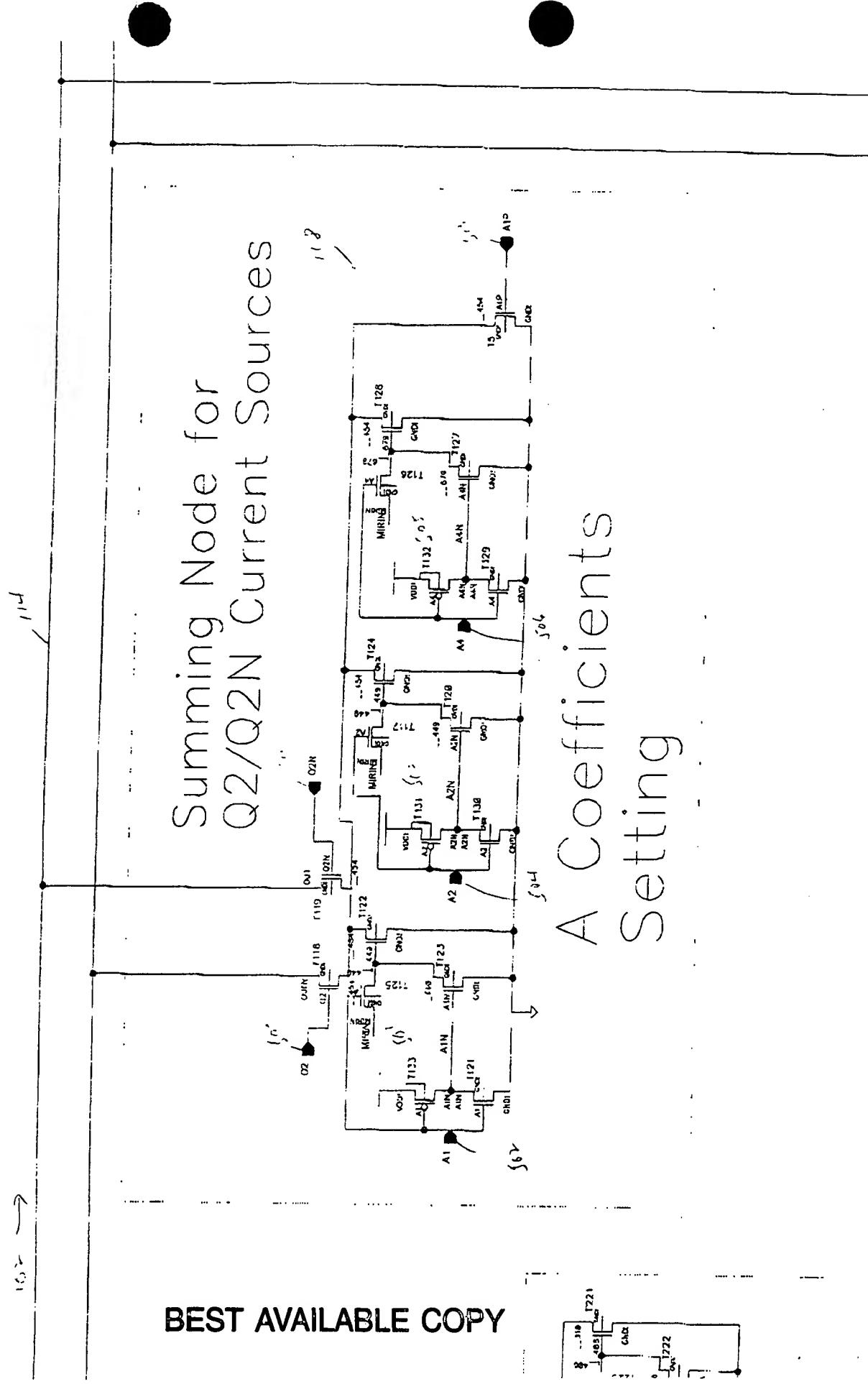
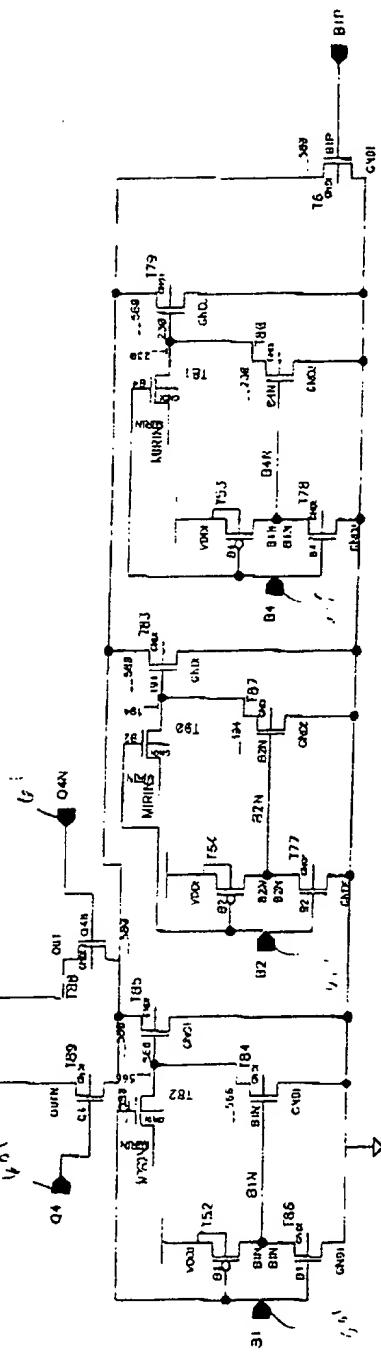


Figure 6. *Scanning electron micrograph of the surface of the*

1137

114

Summing Node for Q4/Q4N Current Sources



Setting B Coefficients

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Figure 7 

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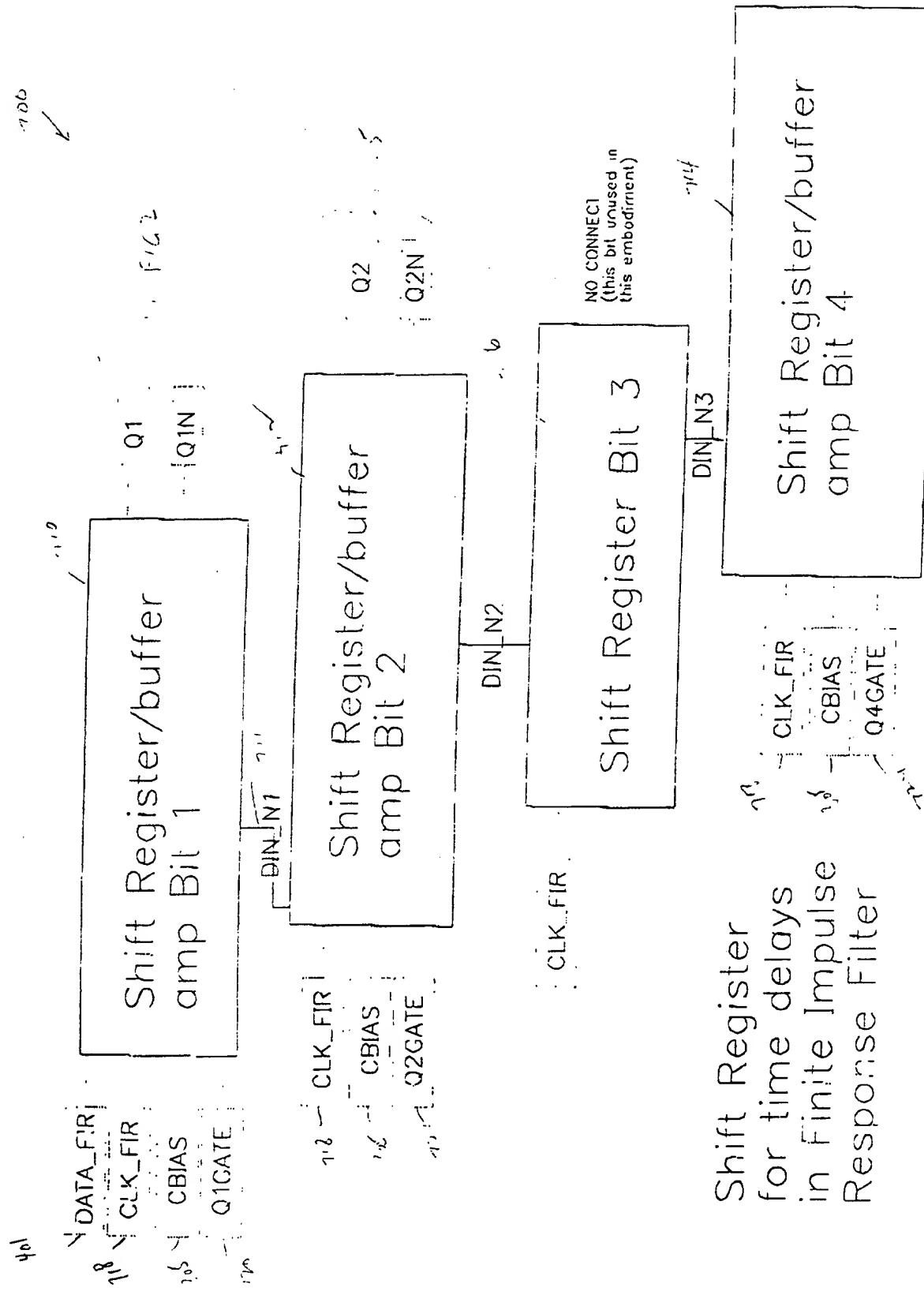
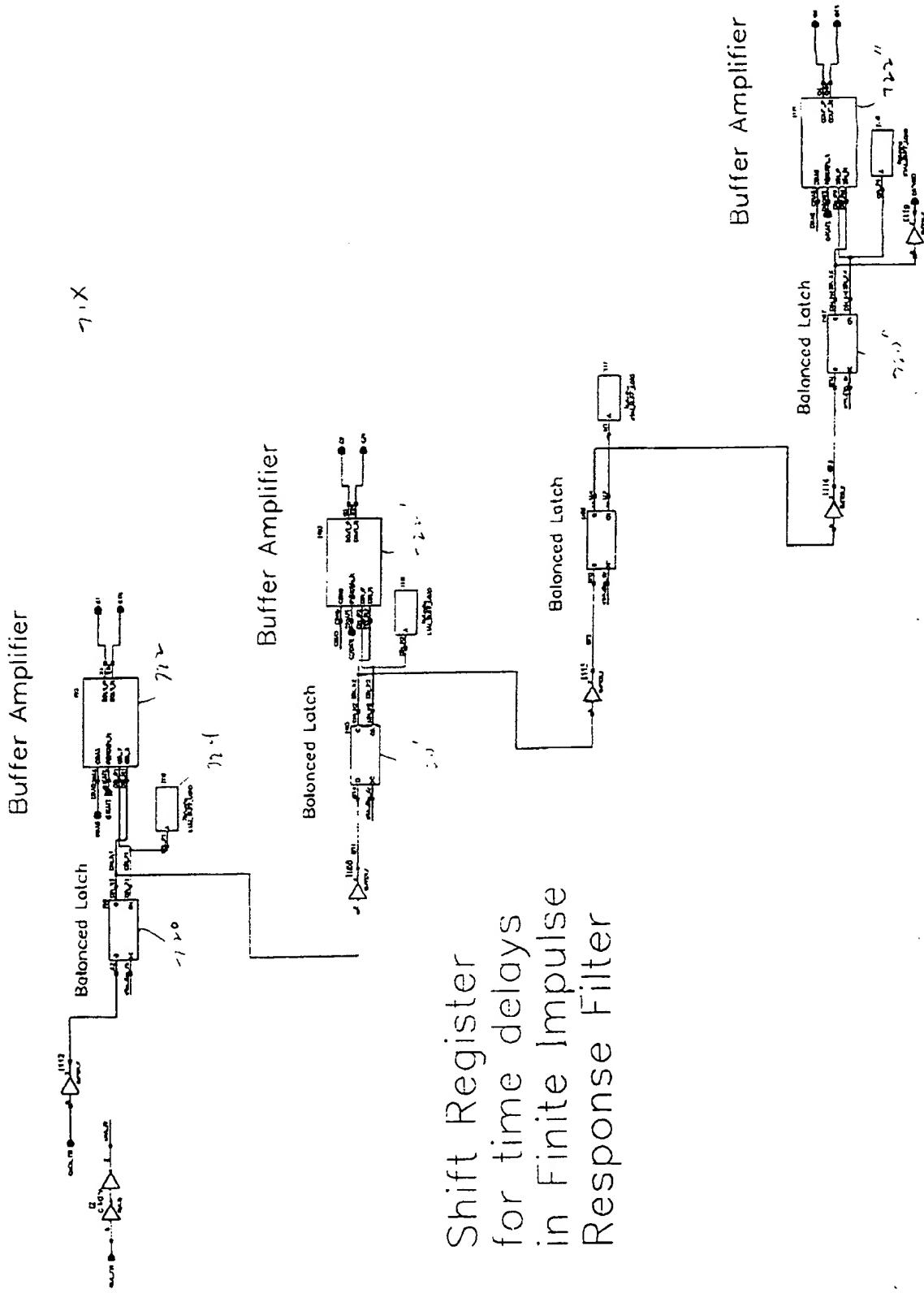


Figure 7A



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Figure 7B

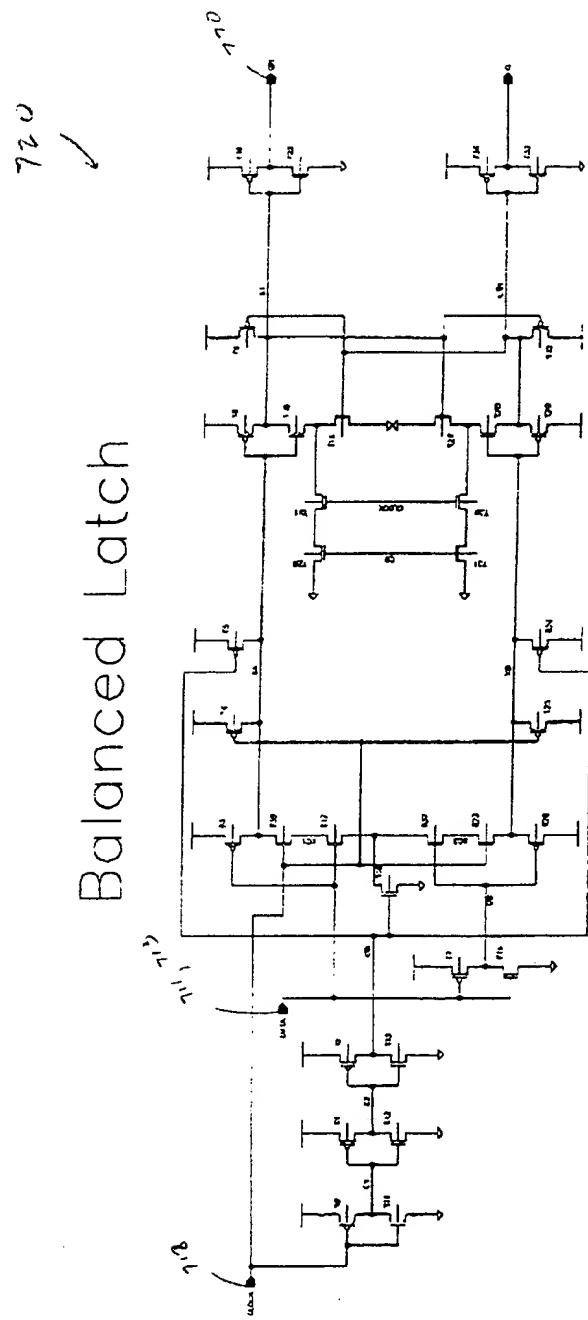
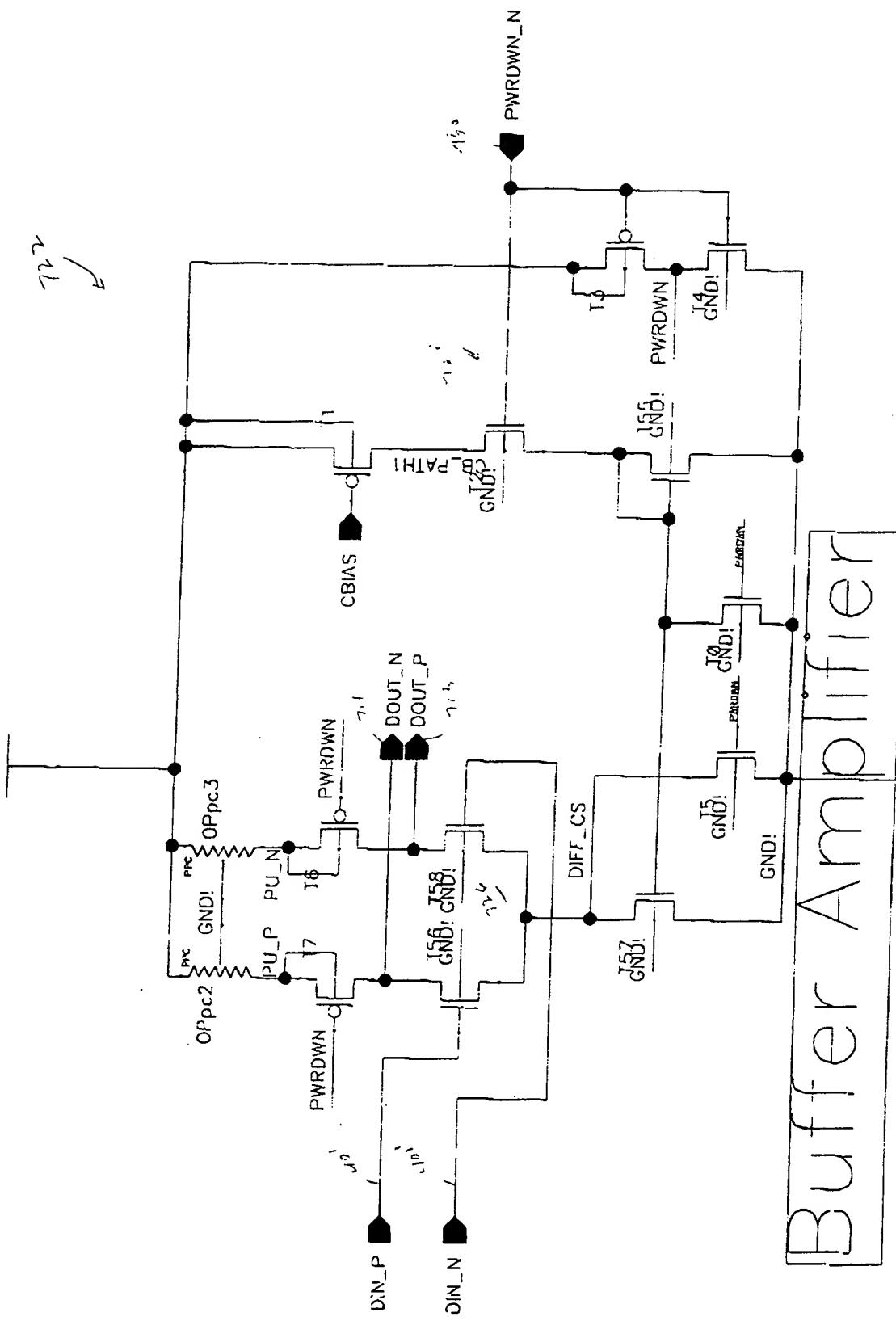


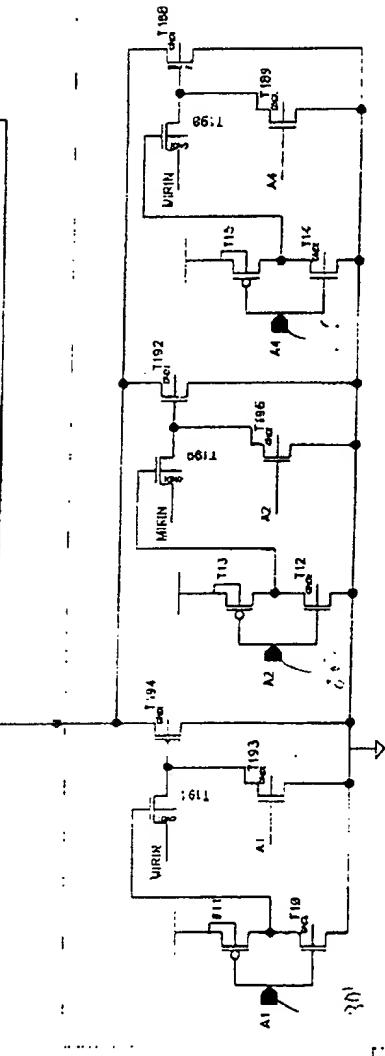
Figure 7C



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Figure 8

Summing Node for
Q1/Q1N Current Sources



$\begin{bmatrix} V_{IRIN} \\ A_1 \\ A_2 \\ A_4 \end{bmatrix}$

A Coefficient Level
Compensation

2

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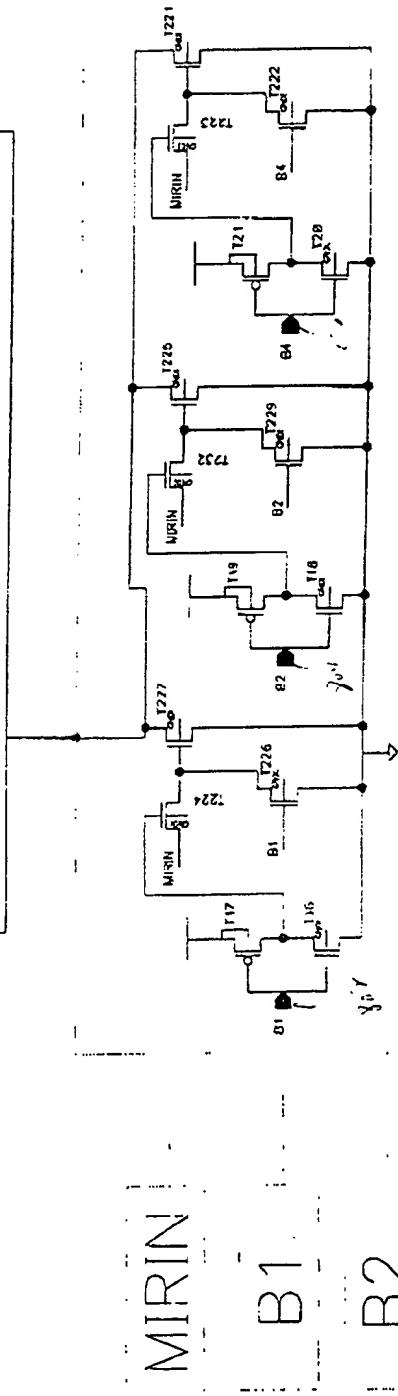
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3

Figure 8A

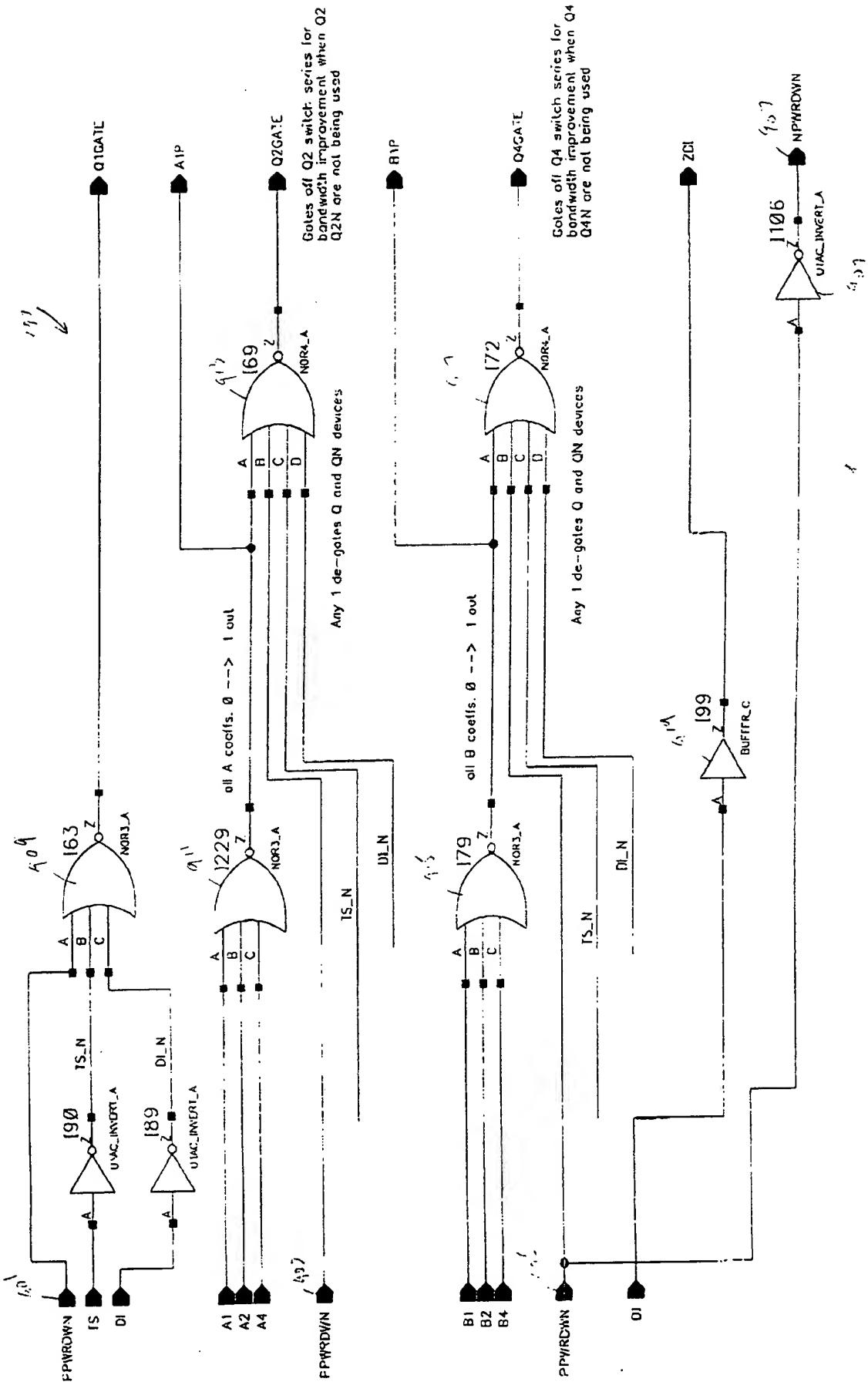
Summing Node for
Q1/Q1N Current Sources

140



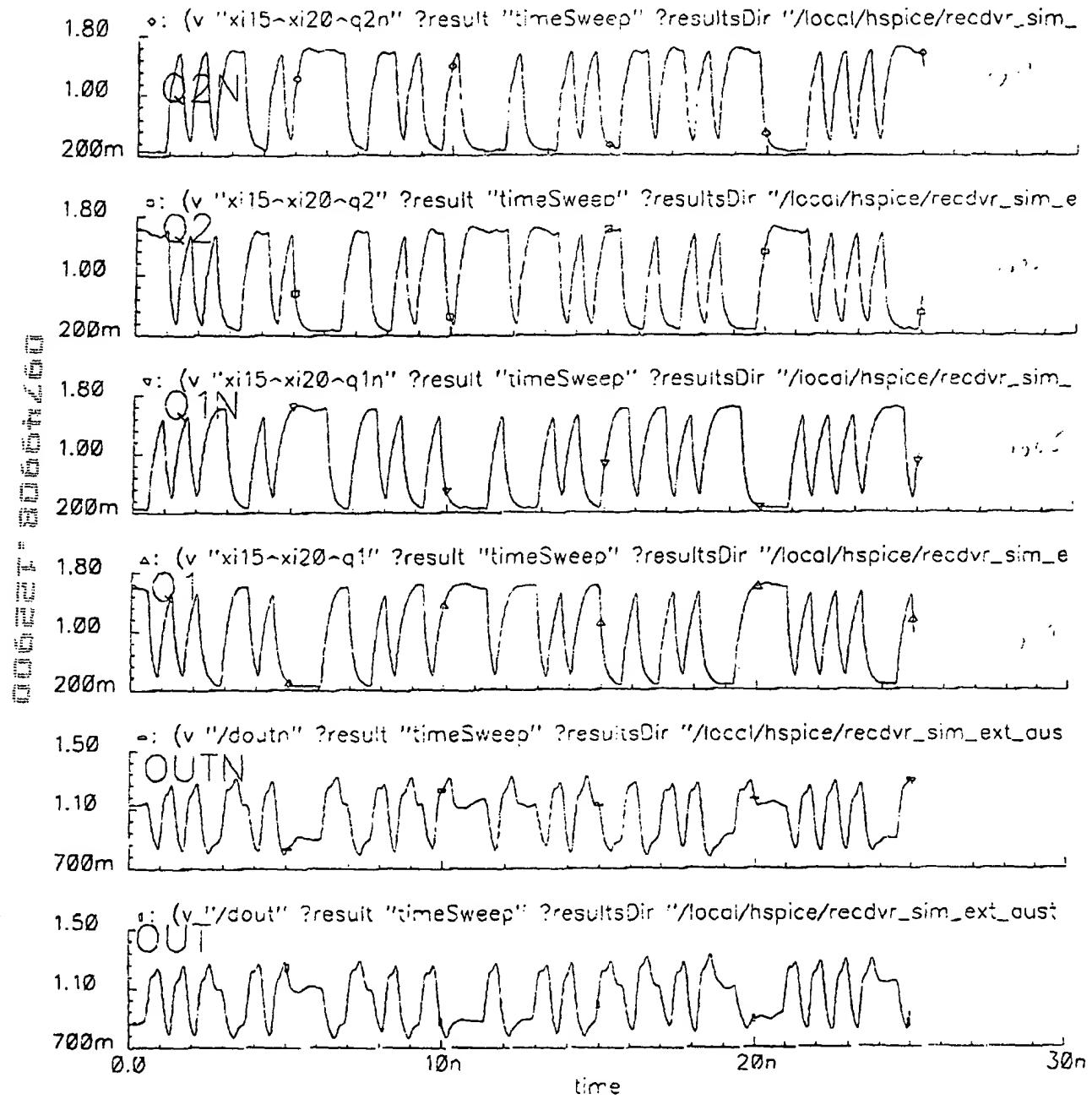
B Coefficient Level
Compensation

Figure 9 and 10



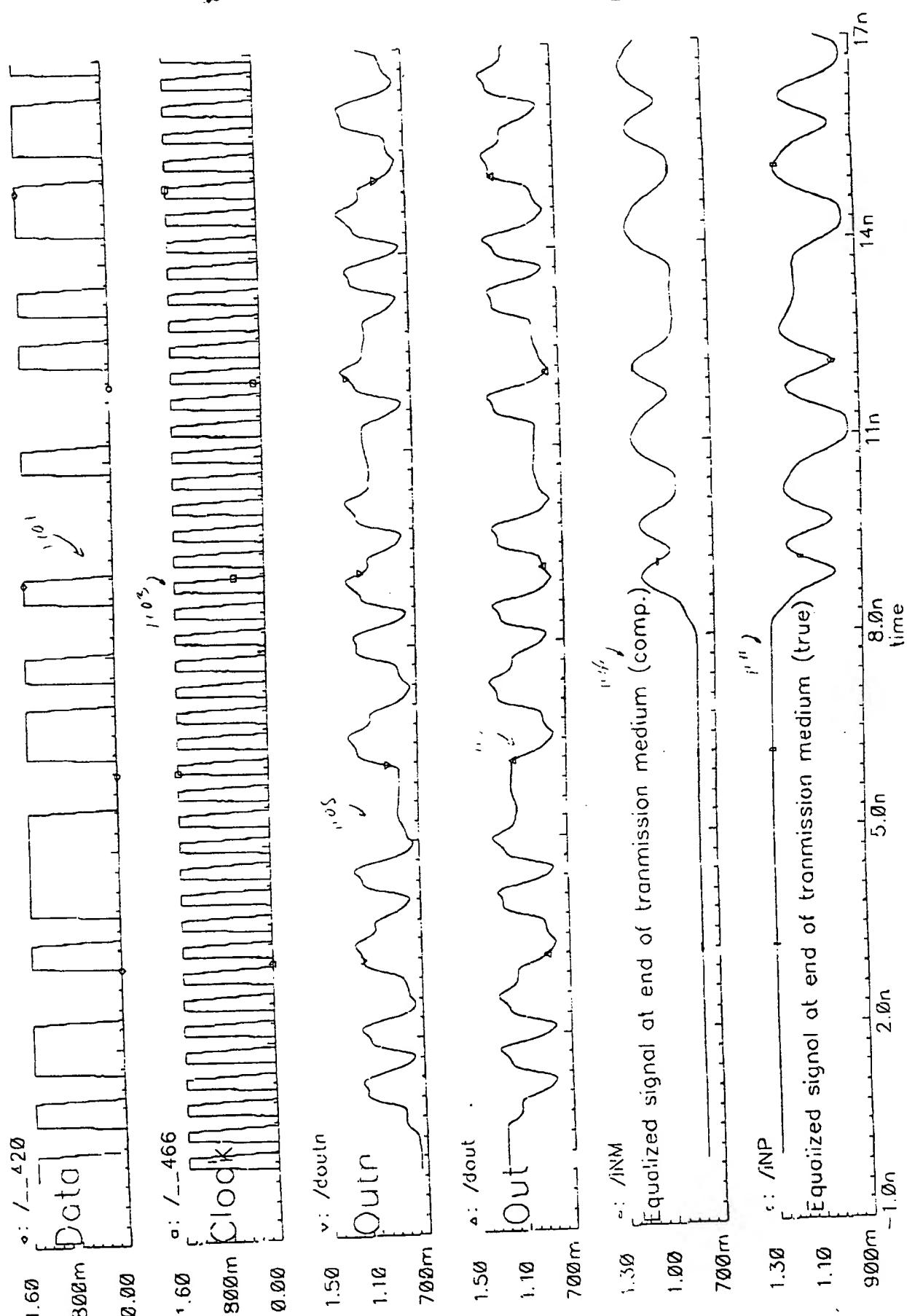
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Figure 10



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Figure 11.20
Transient Response



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